徵格半导体
ZYNALOG

PRODUCT
SELECTION GUIDE
产品选型手册

连接真实与数字，创造简易和品质

Real and Digital，Simple and Quality

徵格半导体【Zynalog Semiconductor】是国内领先的全栈高性能模拟信号链芯片设计研发企业。

徵格以全正向自主研发的ADC、OPA、REF 等近百款高性能芯片，筑就起科技与工业领域的坚实基石，深度赋能众多行业头部企业。凭借卓越性能与顶尖可靠性，徵格正加速推动高端模拟信号链芯片的国产化进程。

未来，我们将持续拓展产品线，构建起覆盖更广、性能更强的信号链解决方案，与合作伙伴共同引领产业智能化新变革。

50+
高速高精度模数转换芯片（ADC）

80+
项系列产品

200+
深度合作企业

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Analog-to-Digital Converter
模数转换器

ZYNALOG Analog-to-Digital Converter	Type	Sample Rate (MHz)	Bits	Channel	Bandwidth (MHz)	Interface	Power Comsuption (mW)	Package
ZGAD250S14	Pipeline	250	14	1	1500	DDR LVDS	378	QFN40
ZGAD250S12	Pipeline	250	12	1	1500	DDR LVDS	362	QFN40
ZGAD250D14	Pipeline	250	14	2	1500	DDR LVDS	620	QFN64
ZGAD250D12	Pipeline	250	12	2	1500	DDR LVDS	620	QFN64
ZGAD125S14	Pipeline	125	14	1	600	DDR LVDS	265	QFN40
ZGAD125S12	Pipeline	125	12	1	600	DDR LVDS	235	QFN40
ZGAD125D14	Pipeline	125	14	2	600	DDR LVDS	418	QFN64
ZGAD125D14C	Pipeline	125	14	2	600	CMOS	412	QFN64
ZGAD125D12	Pipeline	125	12	2	600	DDR LVDS	400	QFN64
ZGAD65S14C	Pipeline	65	14	1	260	CMOS	172	QFN40
ZGAD65S12C	Pipeline	65	12	1	260	CMOS	167	QFN40
ZGAD65D14C	Pipeline	65	14	2	260	CMOS	315	QFN64
ZGAD65D12C	Pipeline	65	12	2	260	CMOS	307	QFN64
ZGAD7522-25	Pipeline	25	14	2	100	Serial LVDS	170	QFN40
ZGAD7522-65	Pipeline	65	14	2	260	Serial LVDS	330	QFN40
ZGAD5000Q16	CT-Sigma Delta	5000	10~16 (configurable)	4	20-500 (configurable)	LVDS	355/channel	BGA
ZGAD020X	CT-Sigma Delta	210	16	4	52	JESD204B	355/channel	BGA

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Low Noise OPAMP
低噪声运放

ZYNALOG Low Noise OPAMP	Channel	Vin (V)	Band-width (MHz)	Iq (mA)	Offset Voltage Drift (μV/°C)	Offset Voltage (μV)	Voltage Noise (nV/√Hz)@10KHz	Input Bias Current (pA)	Temperature Range (°C)	Package	Compatible Part
ZGA2001	1	2.7-5.5	28	4.7	0.4	10	2.1	10	-40°Cto+125°C	SOT23-5	AD8605 AD8615 OPA322 OPA323 OPA325
ZGA2002	2	2.7-5.5	28	4.7	0.4	10	2.1	10	-40°Cto+125°C	SOIC-8 MSOP8	AD8606 AD8616 AD8656 OPA2322 OPA2323 OPA2325
ZGA2011	1	2.7-5.5	28	4.7	0.4	60	2.1	10	-40°Cto+125°C	SOT23-5	AD8605 AD8615 OPA322 OPA323 OPA325
ZGA2012	2	2.7-5.5	28	4.7	0.4	60	2.1	10	-40°Cto+125°C	SOIC-8 MSOP8	AD8606 AD8616 AD8656 OPA2322 OPA2323 OPA2325

High Speed Fully Differential OPAMP
高速全差分运放

ZYNALOG High Speed Fully Differential OPAMP	Vin (V)	-3dB Band-width (MHz)	Slew Rate (V/μs)	Iq (mA)	THD (HD2/HD3)	Voltage Noise (nV/√Hz)@1MHz	Input Bias Current (μA)	Temperature Range (°C)	Package	Compatible Part
ZGA2501	4.7-5.25	2570	7800	52	-87.2dB/-94.9dB (@100MHz)	1.116	56	-40°Cto+125°C	UQFN-14	LMH6554
ZGA2521	4.7-5.25	2570	7800	52	-87.2dB/-94.9dB (@100MHz)	1.116	56	-40°Cto+125°C	VQFN-16	THS4508 THS4509 THS4511 THS4513 THS4520
ZGA2511	2.7-5.5	500	1100	24	-99dB/-111dB (@10MHz)	5	8	-40°Cto+125°C	SOIC-8 MSOP8	AD8138 AD8139 AD8132 AD8131 THS4551 THS4521

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Voltage Reference
基准电压源

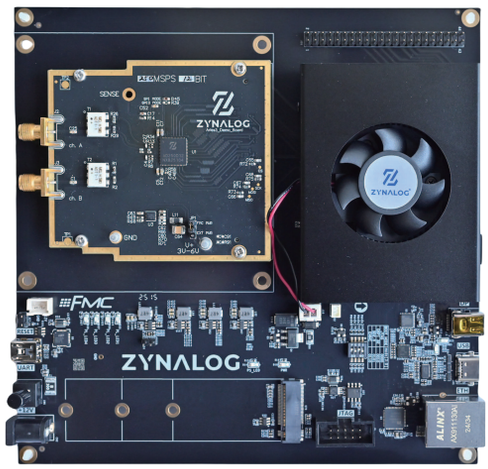
ZYNALOG REF	TC (ppm/℃)	Output Voltage (V)	Vin (Vmax)	Accuracy (max)	Temperature Range (℃)	Package	Compatible Part
ZGR12XX	5	1.25 1.8 2.048 2.5 3.0 3.3 4.096	5.5V	±0.1%	-40℃to+125℃	SOP-8	REF50XX ADR42X ADR43X ADR44X ADR45XX
ZGR11XX	15	1.25 1.8 2.048 2.5 3.0 3.3 4.096	5.5V	±0.1%	-40℃to+125℃	SOT23-3 SOT23-5 SOT23-6	REF31XX
ZGR10XX	30	1.25 1.8 2.048 2.5 3.0 3.3 4.096	5.5V	±0.2%	-40℃to+125℃	SOT23-3 SOT23-5 SOT23-6	REF30XX REF33XX

Interface LVDS
LVDS接口芯片

ZYNALOG Interface LVDS	Type	Channel	Speed (Mbps)	Package	Compatible Part
ZGN4041	LVDS Driver	1ch	400	SOIC-8	ADN4661
ZGN4042	LVDS Driver	2ch	400	SOIC-8	ADN4663
ZGN4044	LVDS Driver	4ch	400	SOIC-16 TSSOP-16	ADN4665
ZGN4141	LVDS Receiver	1ch	400	SOIC-8	ADN4662
ZGN4142	LVDS Receiver	2ch	400	SOIC-8	ADN4664
ZGN4144	LVDS Receiver	4ch	400	SOIC-16 TSSOP-16	ADN4666
ZGN4211-1	M-LVDS transceiver	1ch. Type1	100	SOIC-8	DS91D176 SN65MLVD200 SN65MLVD201 ADN4690E ADN4691E
ZGN4211-2	M-LVDS transceiver	1ch. Type2	100	SOIC-8	DS91C176 SN65MLVD204 SN65MLVD206B ADN4694E ADN4696E

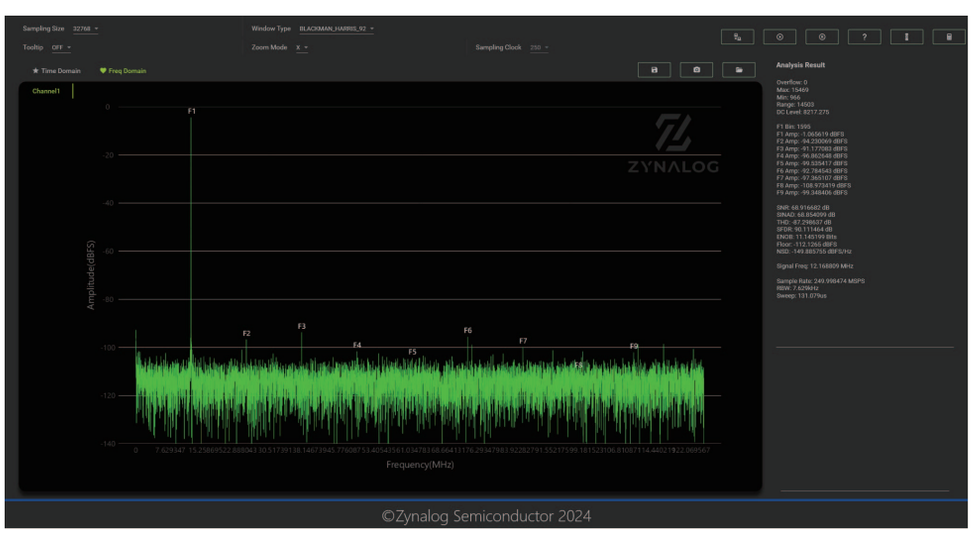
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开发套件
ZGAries



- ZGAries FMC 子板（微格高速 ADC 及其外围电路，配置可选）
- 基于 Xilinx 的 FPGA 开发板，FPGA 开发板由微格提供。同时，可以兼容的第三方 FPGA 开发板比如 Zedboard
- 当外部信号资源不足时，时钟通道信号可以方便的切换为采用 FPGA 开发板提供输出
- 支持 SD/TF 闪存卡插槽以提供 Linux kernel 以及微格 ADC 产品驱动程序

配套工具软件
ZADSCOPE

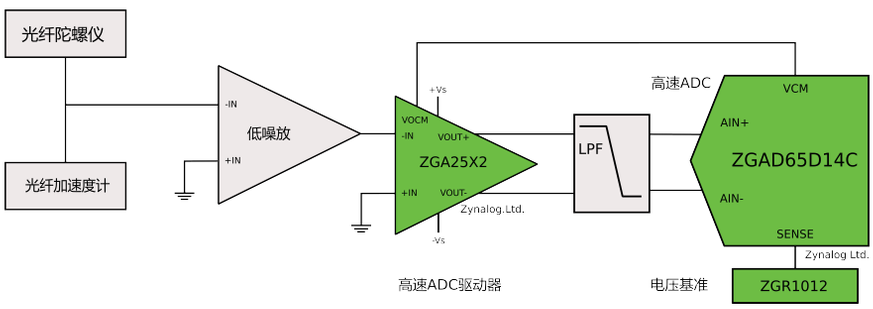


- ZADScope 是微格半导体 ADC 产品开发套件内包括的配套工具软件
- ZADScope 可以提供便捷的实时的 ADC 信号时域/频域动态性能分析
- ZADScope 可以帮助开发者在开发过程中方便的进行ADC产品静态性能验证和测试，例如 ADC DNL/INL指标
- ZADScope 提供友好的图形界面帮助快速配置和调整 ADC 寄存器以适应不同的需求和场景

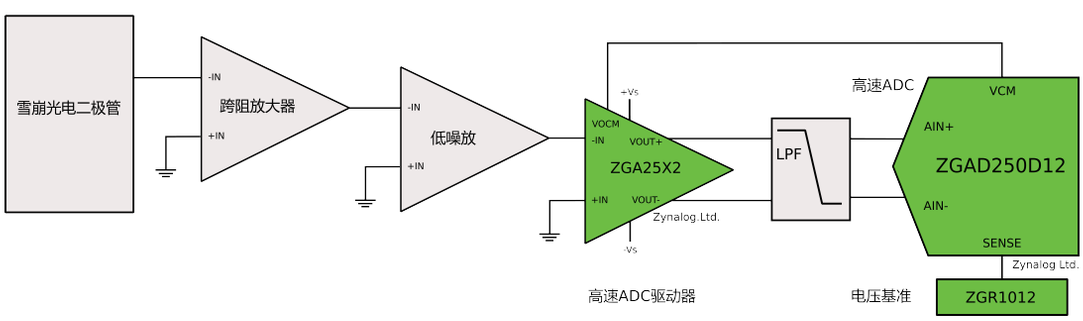
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Application Block Diagram
应用框图(部分)

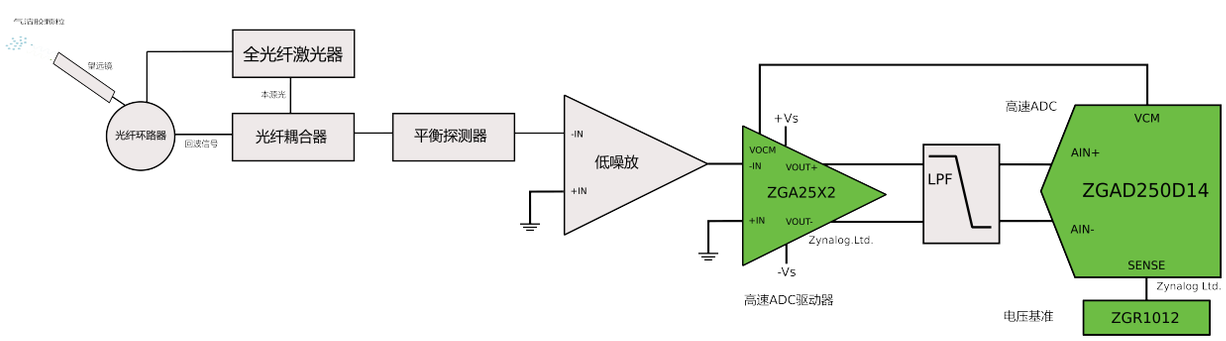
光纤惯性导航方案



分布式光纤测温（DTS）方案



激光测风雷达方案



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