General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® S series is optimized for its switching characteristics to achieve aggressive EMI standards. It is easy to use for smaller power supply systems to meet the both efficiency and EMI standards.

Features

GreenMOS®

RoHS compliant

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity

Applications

- LED lighting
- Charger
- Adapter
- Telecom power
- Server power
- Solar/UPS

Key Performance Parameters

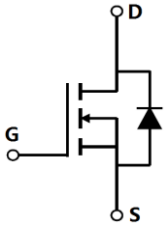
Parameter	Value	Unit
$V_{DS,}$ @ $T_{j(max)}$	650	V
$I_{D,\ pulse}$	60	A
$R_{DS(ON),\ max}$ @ V_{GS}=10V	200	mΩ
Q_g	72.9	nC

Marking Information

Product Name	Package	Marking
OSG60R200PSZF	TO220	OSG60R200PSZ

Package & Pin Information





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Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	600	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	20	A
Continuous drain current[1], T_C=100 °C		12.6	
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	60	A
Continuous diode forward current[1], T_C=25 °C	I_S	20	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	60	A
Power dissipation[3], T_C=25 °C	P_D	151	W
Single pulsed avalanche energy[5]	E_{AS}	176	mJ
MOSFET dv/dt ruggedness, V_{DS}=0…480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}=0…480 V, I_{SD}≤I_D	dv/dt	50	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.83	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	600			V	V_{GS}=0 V, I_D=250 μA
Gate threshold voltage	$V_{GS(th)}$	3.0		4.5	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		0.16	0.20	Ω	V_{GS}=10 V, I_D=10 A
			0.37			V_{GS}=10 V, I_D=10 A, T_j=150 °C
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=30 V
				-100		V_{GS}=-30 V
Drain-source leakage current	I_{DSS}			10	μA	V_{DS}=600 V, V_{GS}=0 V
Gate resistance	R_G		17.4		Ω	f=1 MHz, Open drain

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		1920		pF	V_{GS}=0 V, V_{DS}=50 V, f=100 kHz
Output capacitance	C_{oss}		98.6		pF	
Reverse transfer capacitance	C_{rss}		2.3		pF	
Turn-on delay time	$t_{d(on)}$		41.9		ns	V_{GS}=10 V, V_{DS}=400 V, R_G=2 Ω, I_D=10 A
Rise time	t_r		68.2		ns	
Turn-off delay time	$t_{d(off)}$		77.7		ns	
Fall time	t_f		33.9		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		72.9		nC	V_{GS}=10 V, V_{DS}=400 V, I_D=10 A
Gate-source charge	Q_{gs}		17.2		nC	
Gate-drain charge	Q_{gd}		14		nC	
Gate plateau voltage	$V_{plateau}$		6.5		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=20 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		132		ns	V_R=400 V, I_S=10 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		730		nC	
Peak reverse recovery current	I_{rrm}		10.8		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) V_{DD}=100 V, V_{GS}=10 V, L=80 mH, starting T_j=25 °C.

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Electrical Characteristics Diagrams

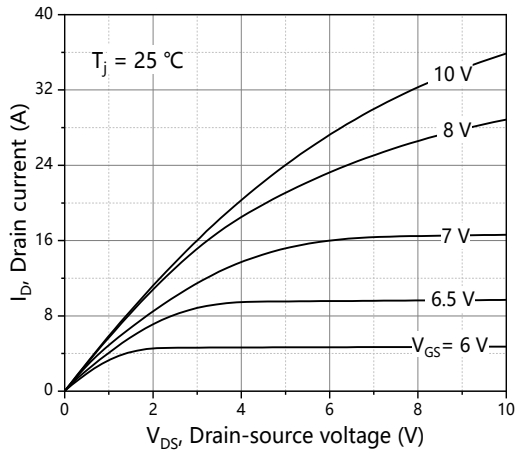

Figure 1. Typ. output characteristics

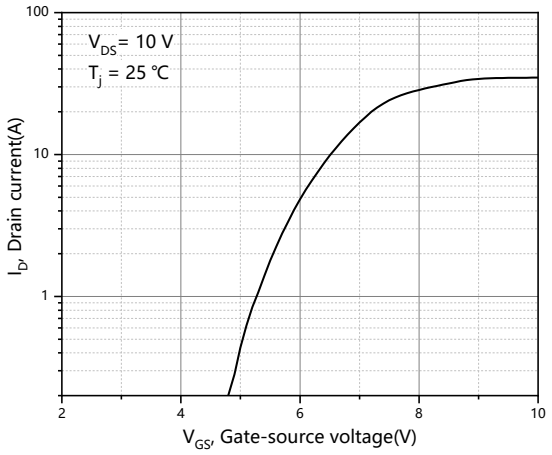

Figure 2. Typ. transfer characteristics

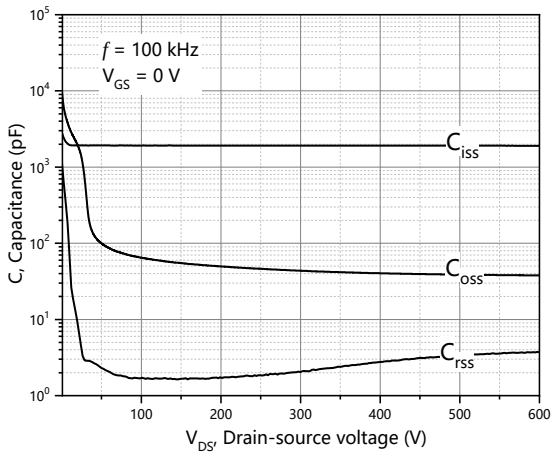

Figure 3. Typ. capacitances

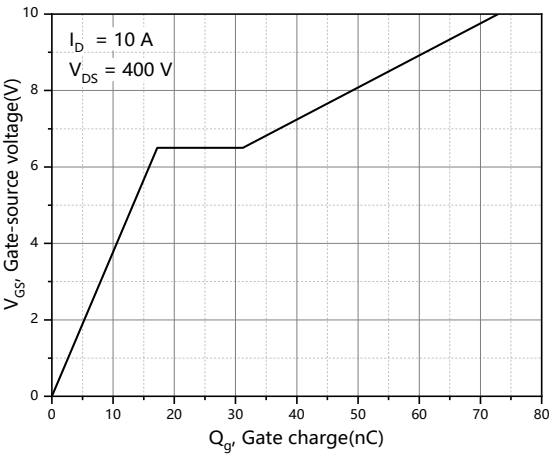

Figure 4. Typ. gate charge

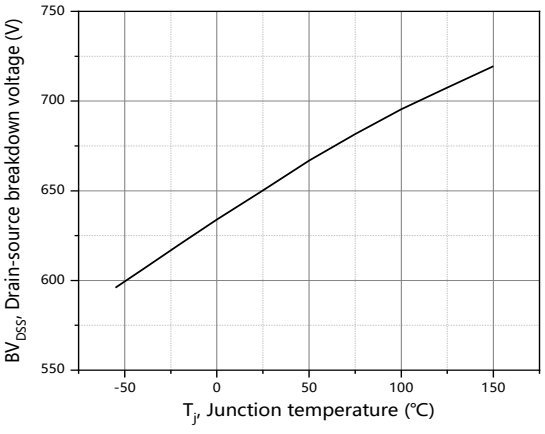

Figure 5. Drain-source breakdown voltage

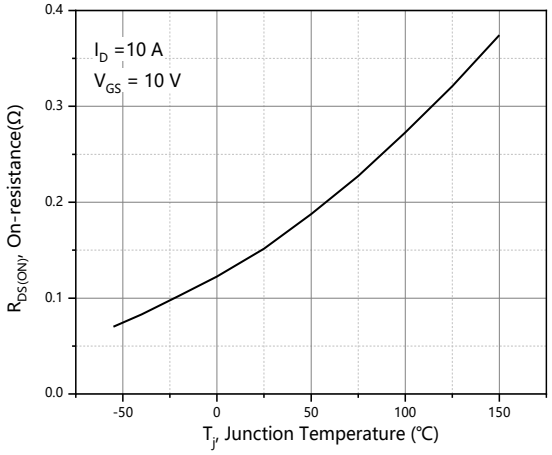

Figure 6. Drain-source on-state resistance

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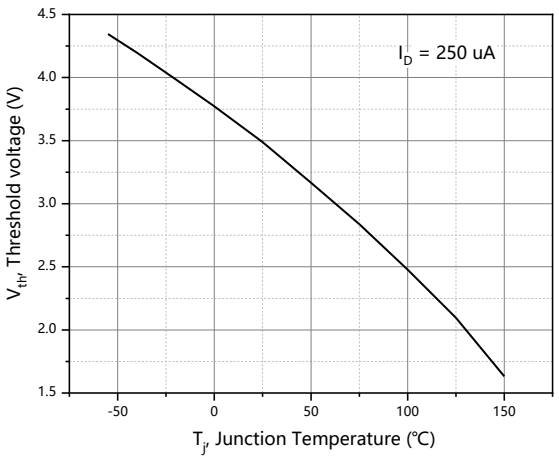

Figure 7. Threshold voltage

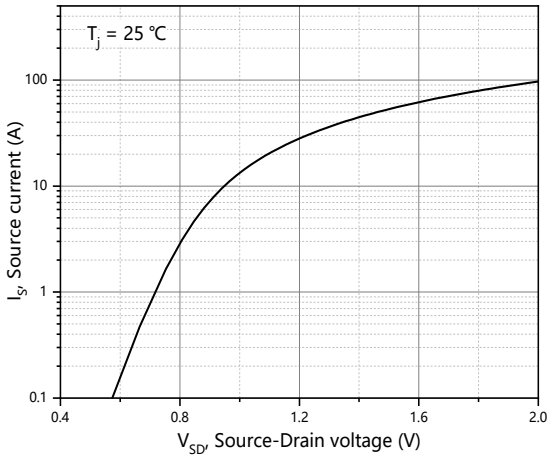

Figure 8. Forward characteristic of body diode

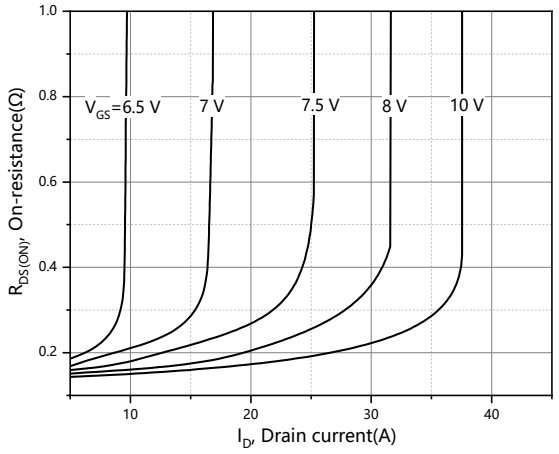

Figure 9. Drain-source on-state resistance

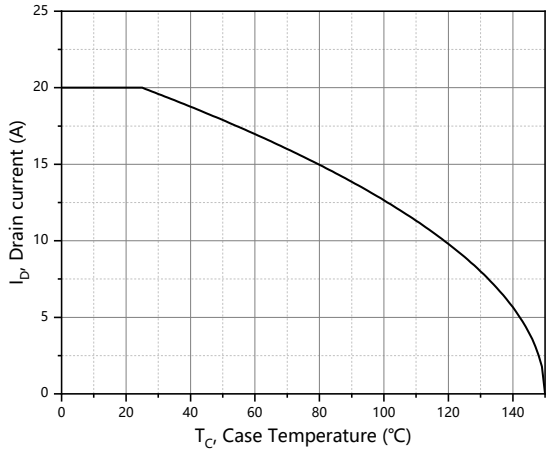

Figure 10. Drain current

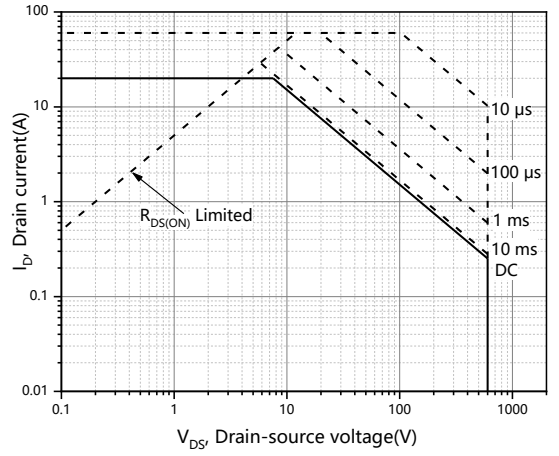

Figure 11. Safe operation area T_C=25 °C

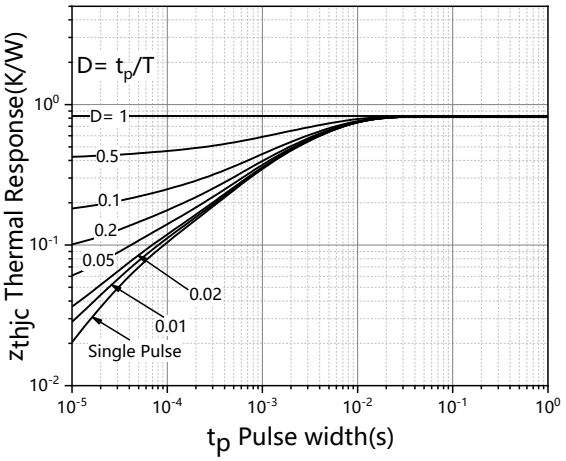

Figure 12. Max. transient thermal impedance

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Test circuits and waveforms

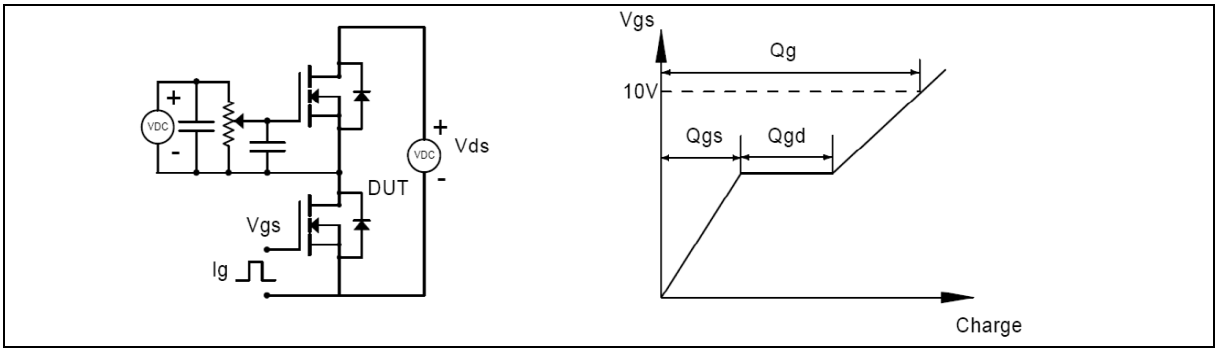


Figure 1. Gate charge test circuit & waveform

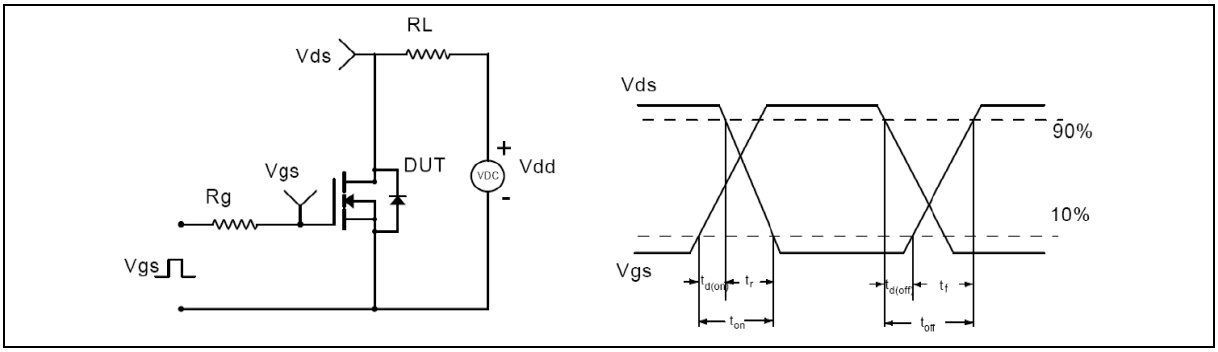


Figure 2. Switching time test circuit & waveforms

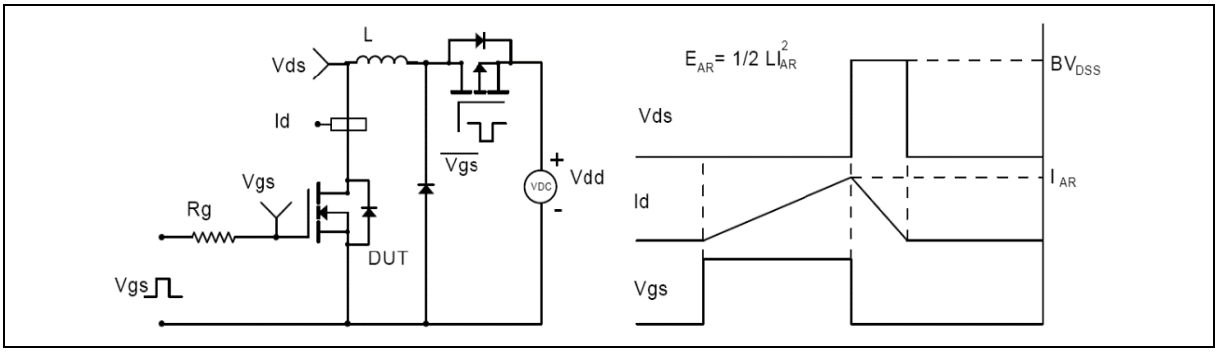


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

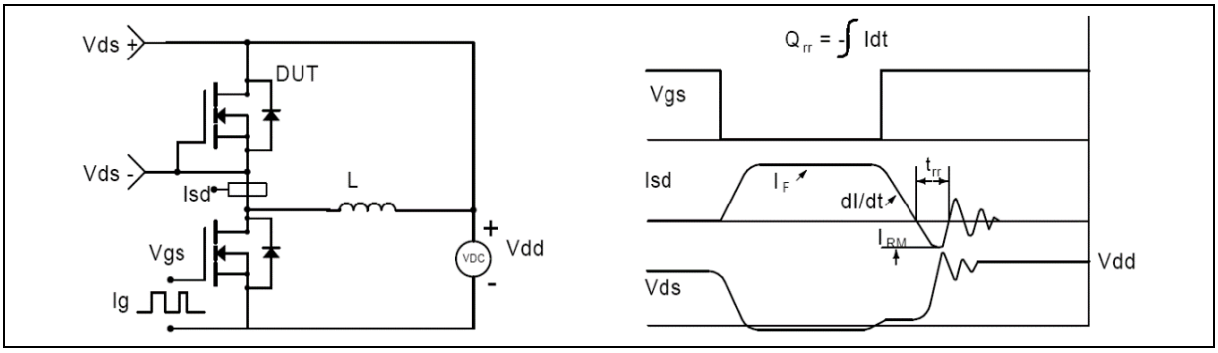
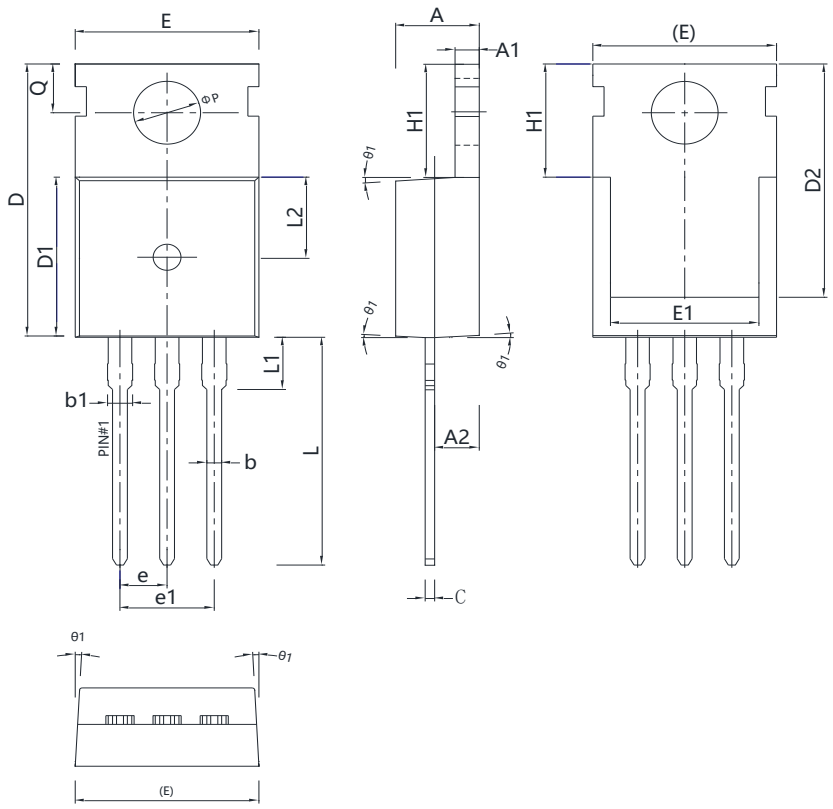


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	-	0.90
b1	1.27	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54 BSC		
e1	5.08 BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.60 REF		
ΦP	3.55	3.60	3.65
Q	2.73	-	2.87
θ1	1°	3°	5°

Version 1: TO220-J package outline dimension

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Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220-J	50	20	1000	5	5000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG60R200PSZF	TO220	yes	yes	yes

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