General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® E series is optimized for its switching characteristics to achieve balance between EMI and efficiency. It is designed to enable power supply systems to reach the highest efficiency while still meeting EMI standards.

Features

GreenMOS®

RoHS compliant

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity

Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

Key Performance Parameters

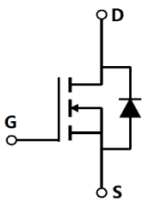
Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	750	V
$I_{D,\ pulse}$	33	A
$R_{DS(ON)\ ,\ max}$ @ V_{GS}=10V	420	mΩ
Q_g	15	nC

Marking Information

Product Name	Package	Marking
OSG70R420DEF	TO252	OSG70R420DE

Package & Pin Information





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Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	700	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	11	A
Continuous drain current[1], T_C=100 °C		7	
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	33	A
Continuous diode forward current[1], T_C=25 °C	I_S	11	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	33	A
Power dissipation[3], T_C=25 °C	P_D	83	W
Single pulsed avalanche energy[5]	E_{AS}	200	mJ
MOSFET dv/dt ruggedness, V_{DS}=0…480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}=0…480 V, I_{SD}≤I_D	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.5	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	700			V	V_{GS}=0 V, I_D=250 μA
Gate threshold voltage	$V_{GS(th)}$	2.9		3.9	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		0.36	0.42	Ω	V_{GS}=10 V, I_D=5 A
			0.92			V_{GS}=10 V, I_D=5 A, T_j=150 °C
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=30 V
				-100		V_{GS}=-30 V
Drain-source leakage current	I_{DSS}			1	μA	V_{DS}=700 V, V_{GS}=0 V
Gate resistance	R_G		30		Ω	f= 1 MHz, Open drain

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		761		pF	V_{GS}=0 V, V_{DS}=50 V, f=1 MHz
Output capacitance	C_{oss}		62		pF	
Reverse transfer capacitance	C_{rss}		2.5		pF	
Turn-on delay time	$t_{d(on)}$		31		ns	V_{GS}=10 V, V_{DS}=400 V, R_G=2 Ω, I_D=6 A
Rise time	t_r		18.8		ns	
Turn-off delay time	$t_{d(off)}$		58.3		ns	
Fall time	t_f		6.5		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		15		nC	V_{GS}=10 V, V_{DS}=400 V, I_D=6 A
Gate-source charge	Q_{gs}		3.7		nC	
Gate-drain charge	Q_{gd}		5.8		nC	
Gate plateau voltage	$V_{plateau}$		5.7		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=11 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		239.7		ns	V_R=400V, I_S=6 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		2.5		μC	
Peak reverse recovery current	I_{rrm}		21.2		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5) V_{DD}=100 V, V_{GS}=10 V, L=80 mH, starting T_j=25 °C.

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Electrical Characteristics Diagrams

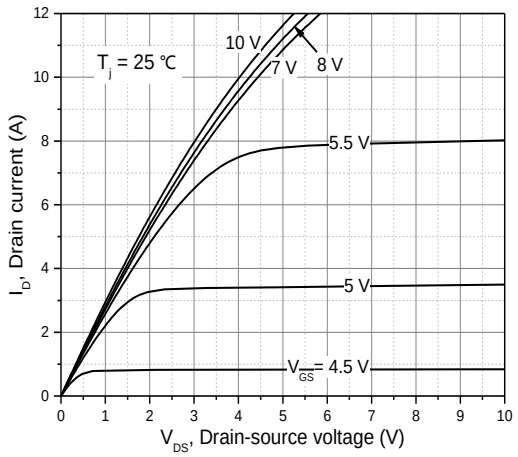


Figure 1. Typ. output characteristics

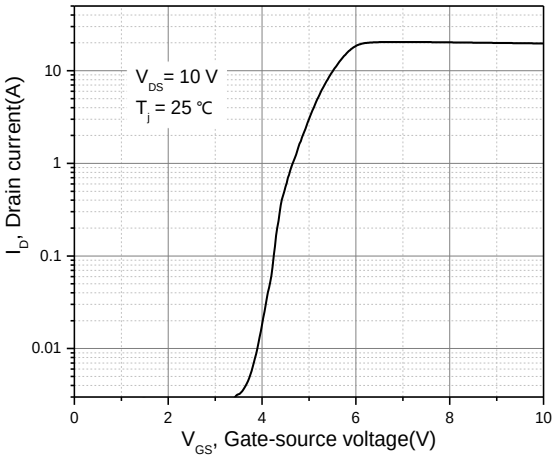


Figure 2. Typ. transfer characteristics

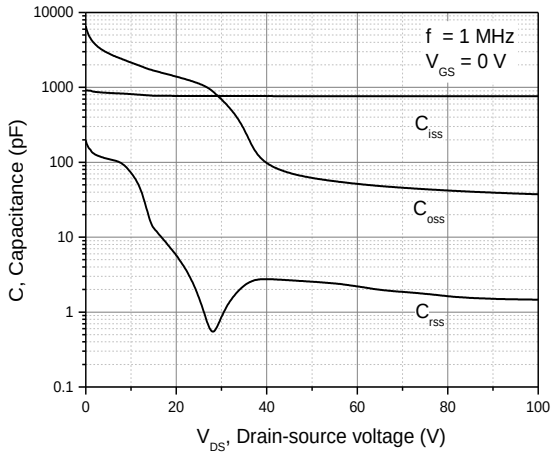


Figure 3. Typ. capacitances

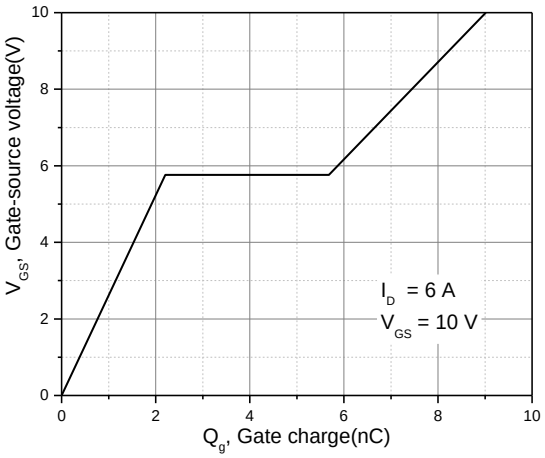


Figure 4. Typ. gate charge

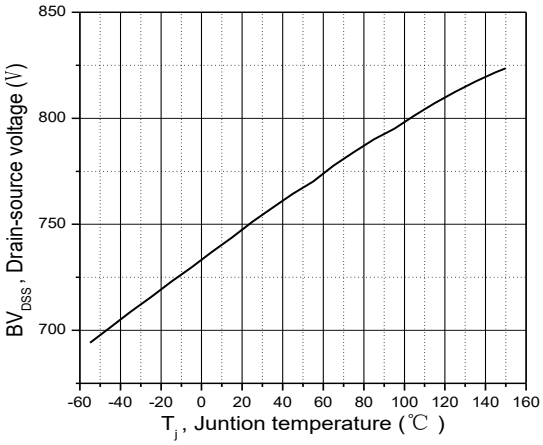


Figure 5. Drain-source breakdown voltage

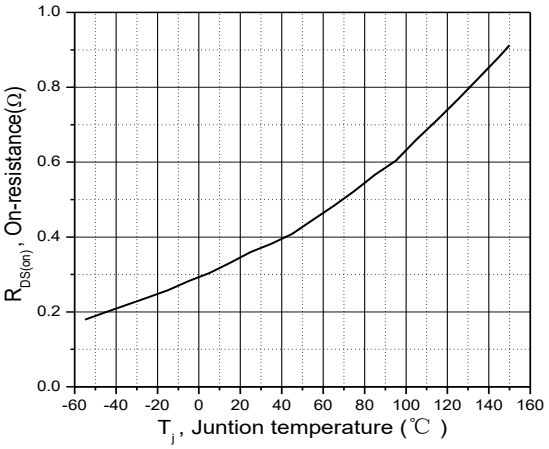


Figure 6. Drain-source on-state resistance

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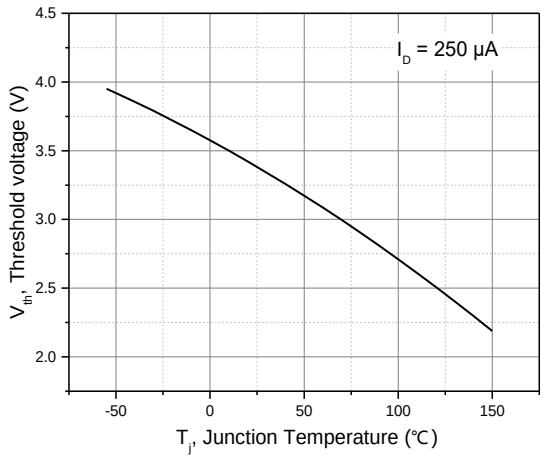


Figure 7. Threshold voltage

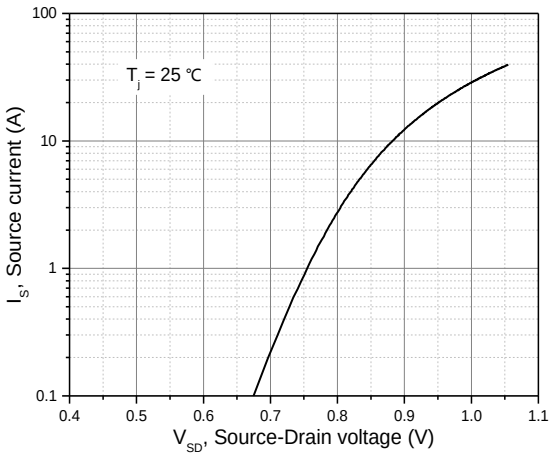


Figure 8. Forward characteristic of body diode

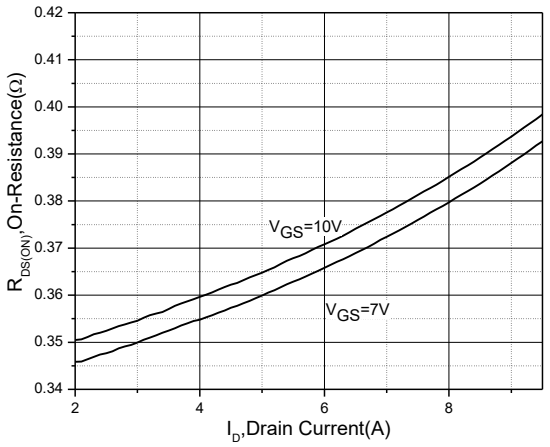


Figure 9. Drain-source on-state resistance

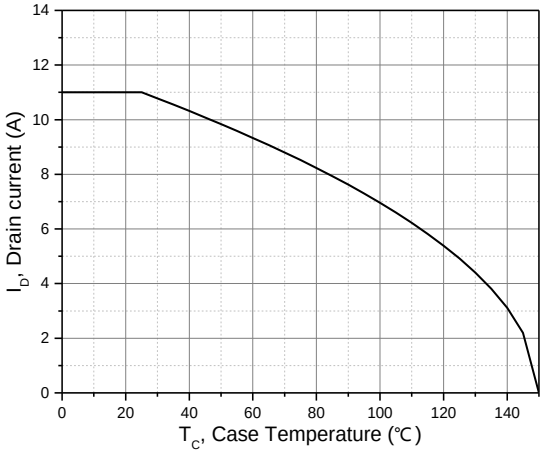


Figure 10. Drain current

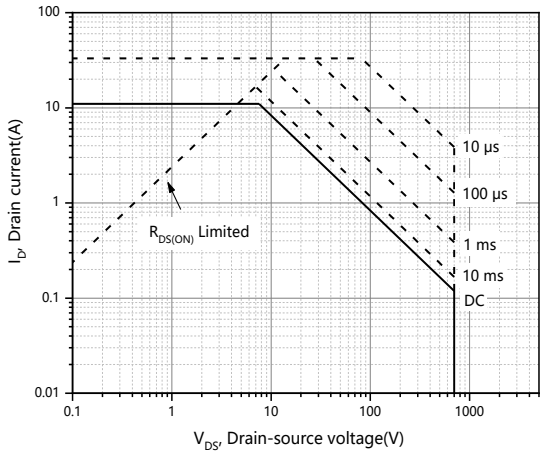


Figure 11. Safe operation area T_C=25 °C

Test circuits and waveforms

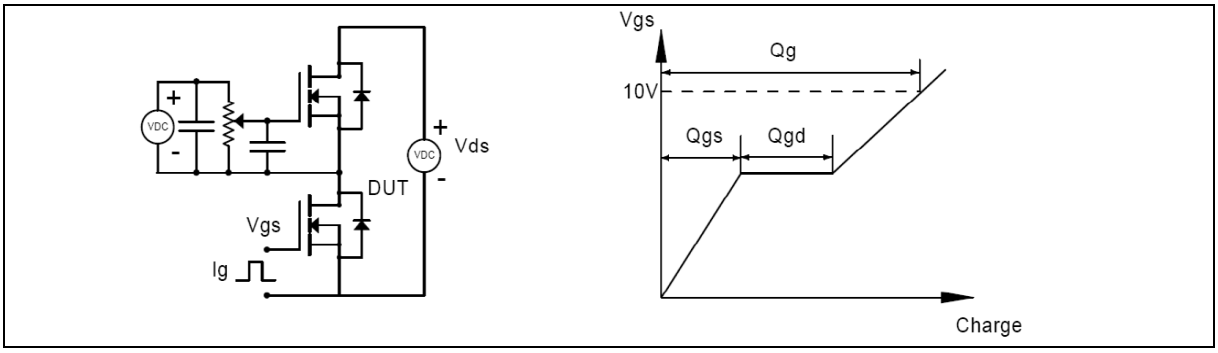


Figure 1. Gate charge test circuit & waveform

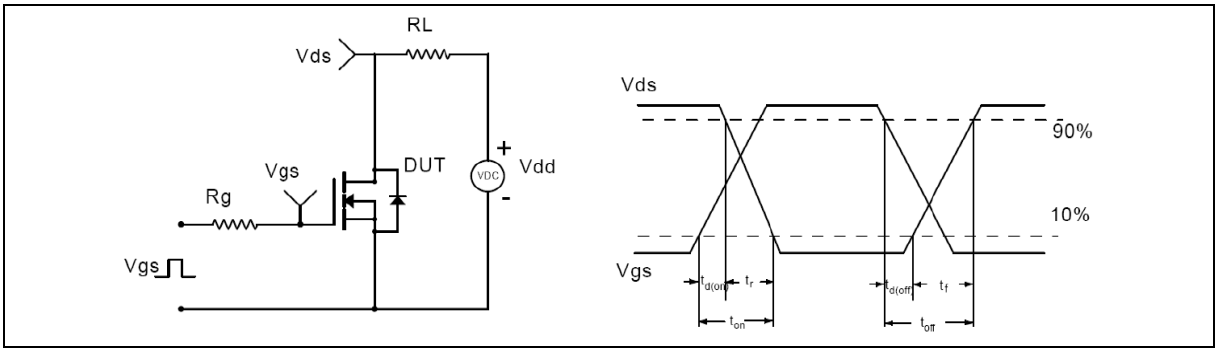


Figure 2. Switching time test circuit & waveforms

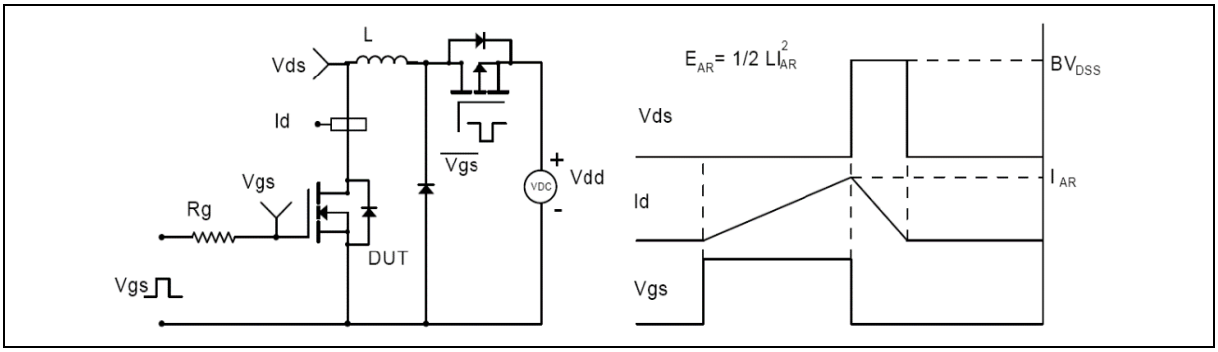


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

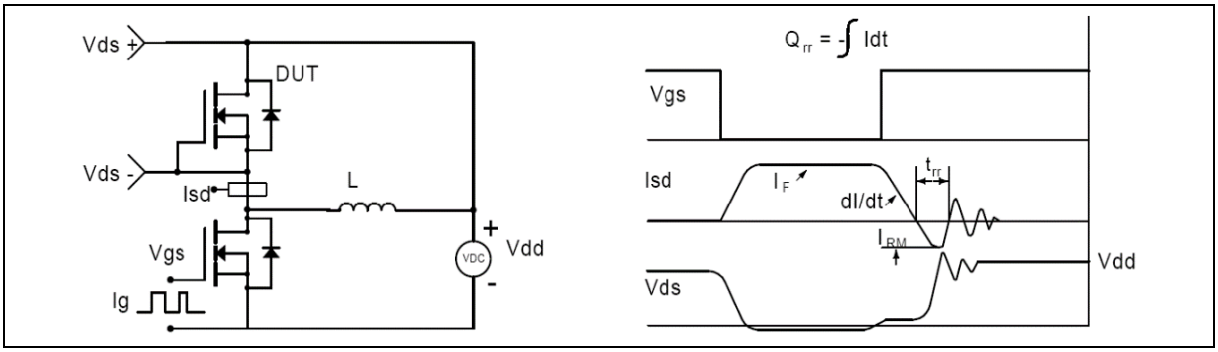
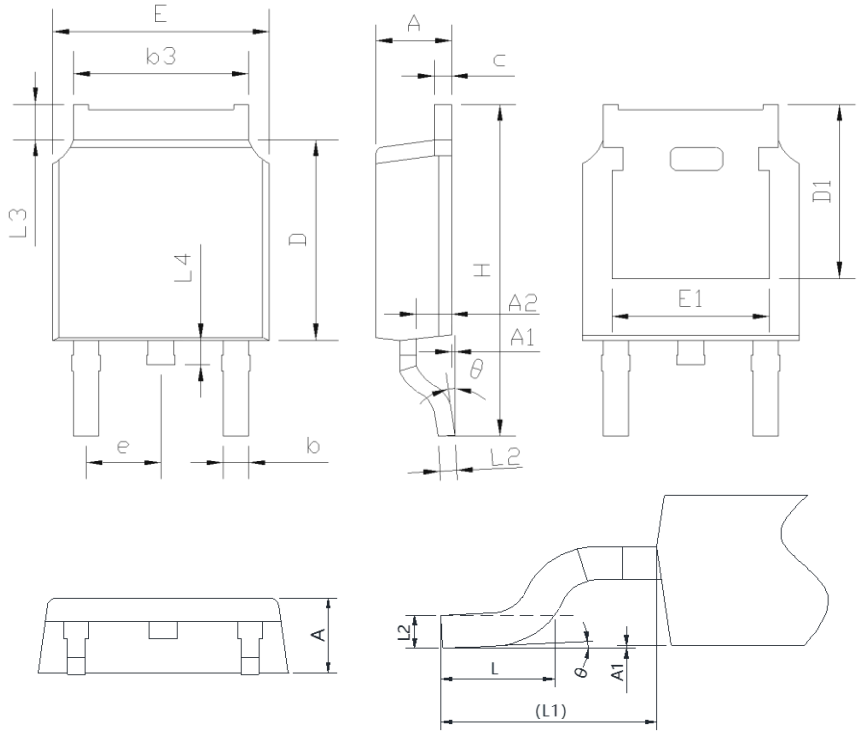


Figure 4. Diode reverse recovery test circuit & waveforms

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Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30 REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286 BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90 REF		
L2	0.51 BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
θ	0°	-	8°

Version 1: TO252-P package outline dimension

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Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-P	2500	2	5000	5	25000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG70R420DEF	TO252	yes	yes	yes

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