General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® S series is optimized for its switching characteristics to achieve aggressive EMI standards. It is easy to use for smaller power supply systems to meet the both efficiency and EMI standards.

Features

GreenMOS®

RoHS compliant

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity

Applications

- LED lighting
- Charger
- Adapter
- Telecom power
- Server power
- Solar/UPS

Key Performance Parameters

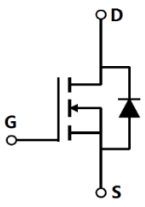
Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	850	V
$I_{D,\ pulse}$	18	A
$R_{DS(ON),\ max}$ @ V_{GS}=10V	380	mΩ
Q_g	18	nC

Marking Information

Product Name	Package	Marking
OSG80R380DSF	TO252	OSG80R380DS

Package & Pin Information





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Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	800	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	11	A
Continuous drain current[1], T_C=100 °C		7	
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	18	A
Continuous diode forward current[1], T_C=25 °C	I_S	11	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	18	A
Power dissipation[3], T_C=25 °C	P_D	130	W
Single pulsed avalanche energy[5]	E_{AS}	122	mJ
MOSFET dv/dt ruggedness, V_{DS}=0…480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}=0…480 V, I_{SD}≤I_D	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.96	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	800			V	V_{GS}=0 V, I_D=250 μA
		850				V_{GS}=0 V, I_D=250 μA, T_j=150 °C
Gate threshold voltage	$V_{GS(th)}$	2.9		3.9	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		0.31	0.38	Ω	V_{GS}=10 V, I_D=5.5 A
			0.93			V_{GS}=10 V, I_D=5.5 A, T_j=150 °C
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=30 V
				-100		V_{GS}=-30 V
Drain-source leakage current	I_{DSS}			10	μA	V_{DS}=800 V, V_{GS}=0 V
Gate resistance	R_G		6		Ω	f=1 MHz, Open drain

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		952		pF	V_{GS}=0 V, V_{DS}=50 V, f=100 kHz
Output capacitance	C_{oss}		58		pF	
Reverse transfer capacitance	C_{rss}		1.3		pF	
Effective output capacitance, energy related	$C_{o(er)}$		37		pF	V_{GS}=0 V, V_{DS}=0 V-400 V
Effective output capacitance, time related	$C_{o(tr)}$		200		pF	
Turn-on delay time	$t_{d(on)}$		11		ns	V_{GS}=10 V, V_{DS}=400 V, R_G=2 Ω, I_D=10 A
Rise time	t_r		6.4		ns	
Turn-off delay time	$t_{d(off)}$		25		ns	
Fall time	t_f		3.7		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		18		nC	V_{GS}=10 V, V_{DS}=400 V, I_D=10 A
Gate-source charge	Q_{gs}		6.6		nC	
Gate-drain charge	Q_{gd}		3.5		nC	
Gate plateau voltage	$V_{plateau}$		5.1		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=11 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		376		ns	V_R=400 V, I_S=10 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		4.6		μC	
Peak reverse recovery current	I_{rrm}		20		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5) V_{DD}=100 V, V_{GS}=10 V, L=75 mH, starting T_j=25 °C.

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Electrical Characteristics Diagrams

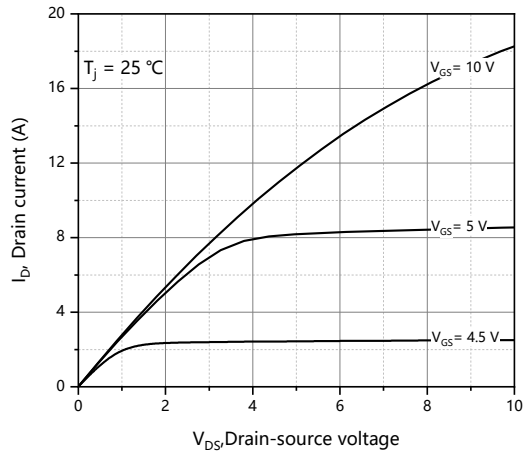


Figure 1. Typ. output characteristics

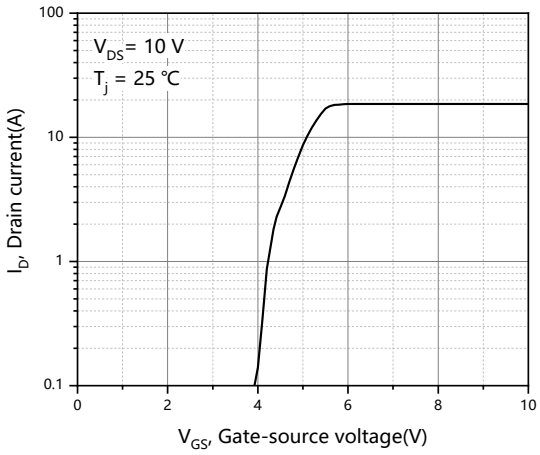


Figure 2. Typ. transfer characteristics

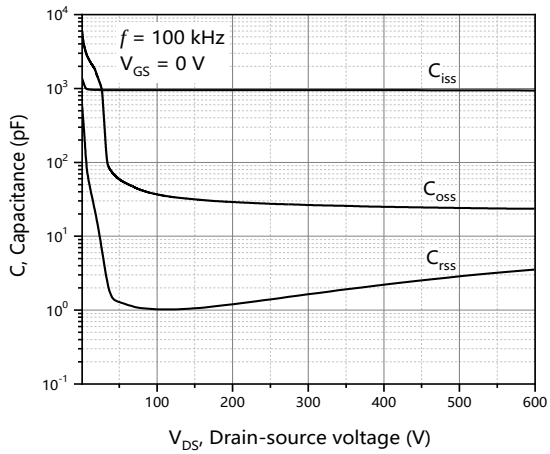


Figure 3. Typ. capacitances

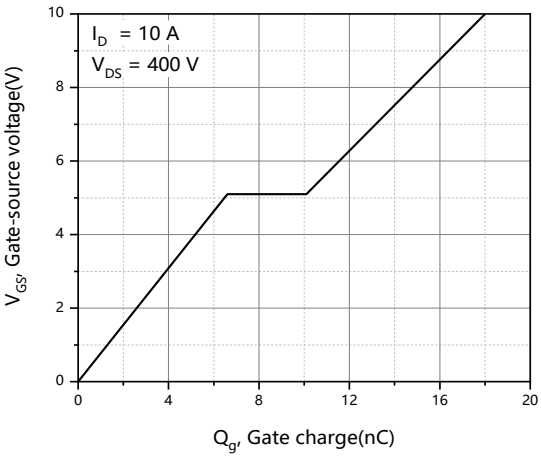


Figure 4. Typ. gate charge

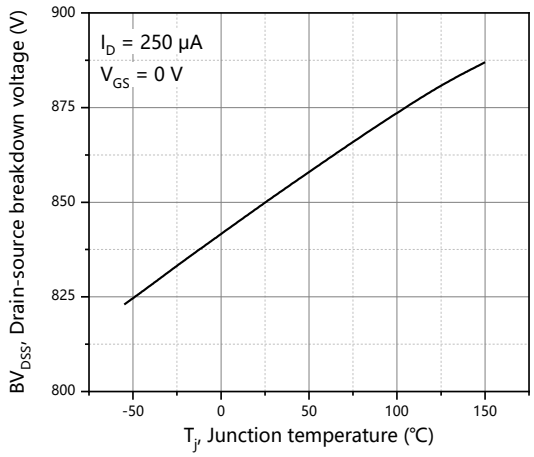


Figure 5. Drain-source breakdown voltage

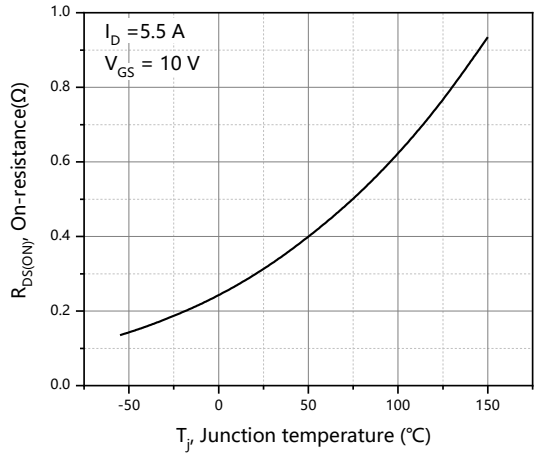


Figure 6. Drain-source on-state resistance

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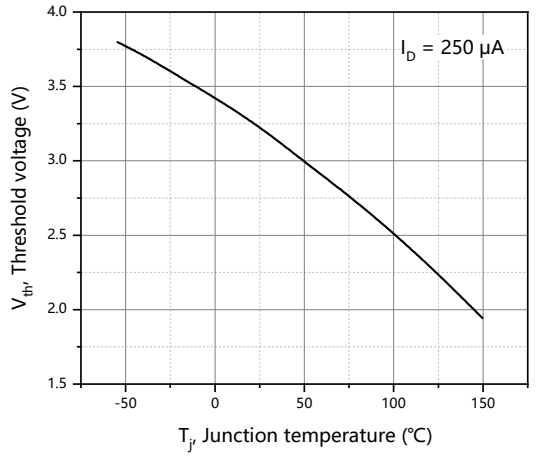


Figure 7. Threshold voltage

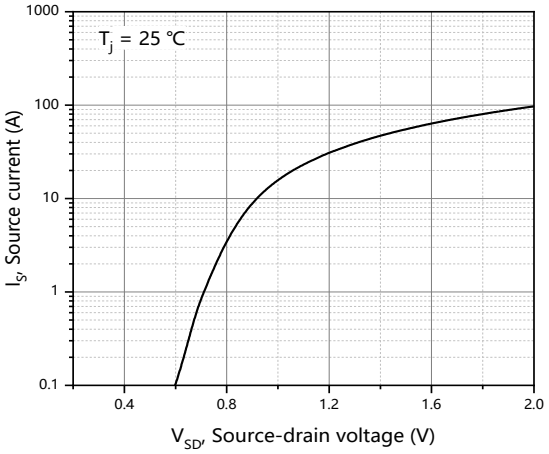


Figure 8. Forward characteristic of body diode

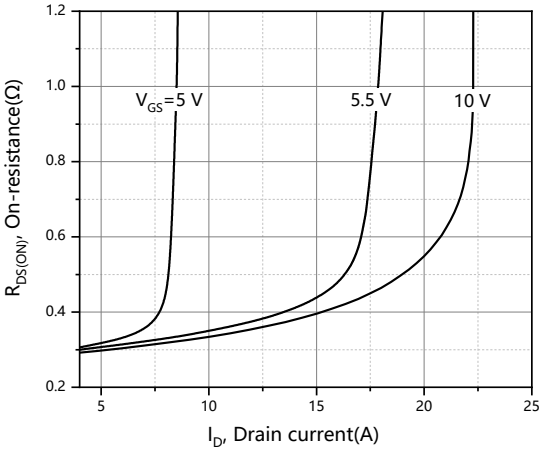


Figure 9. Drain-source on-state resistance

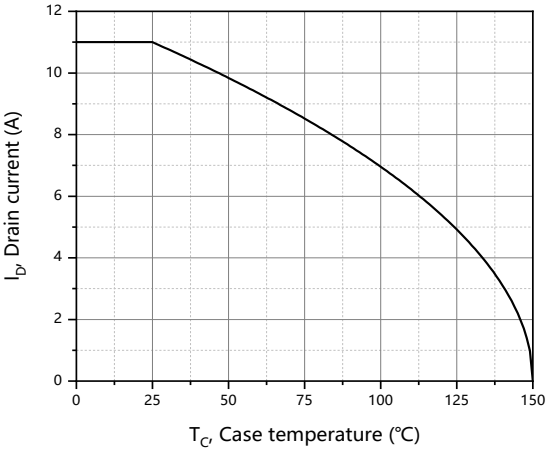


Figure 10. Drain current

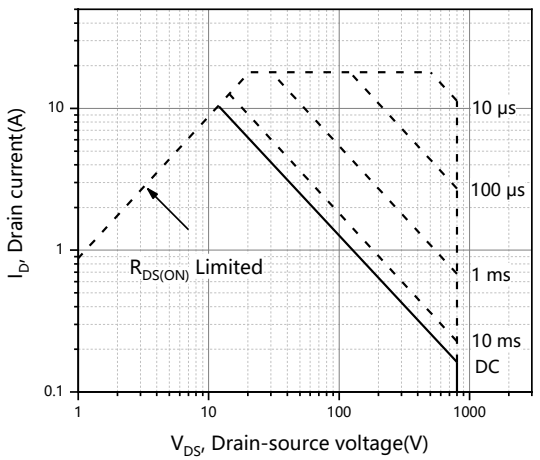


Figure 11. Safe operation area T_C=25 °C

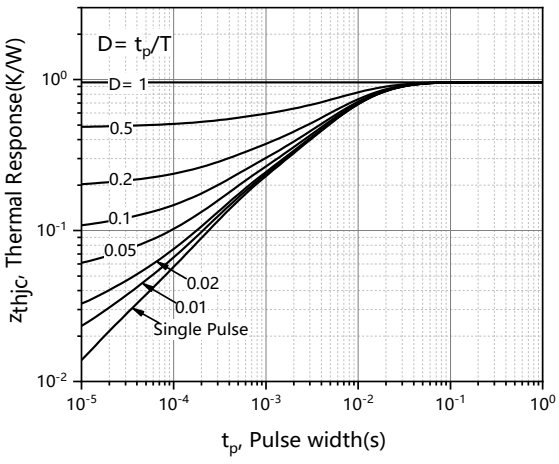


Figure 12. Max. transient thermal impedance

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Test circuits and waveforms

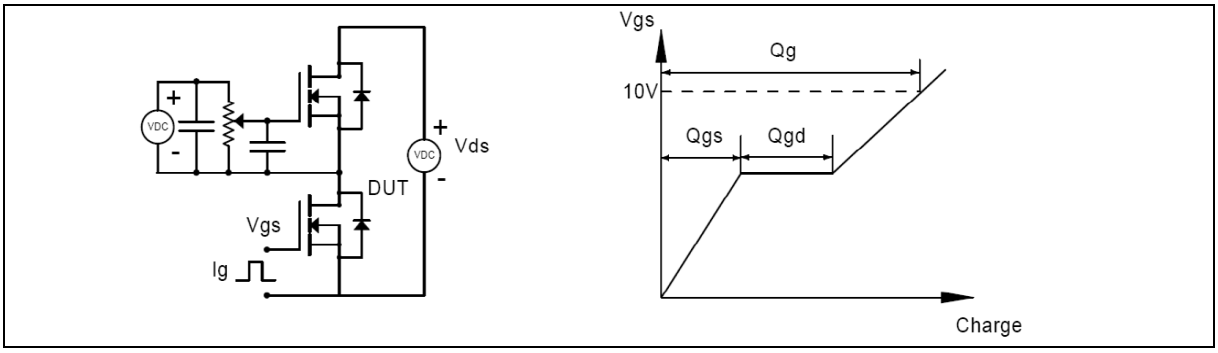


Figure 1. Gate charge test circuit & waveform

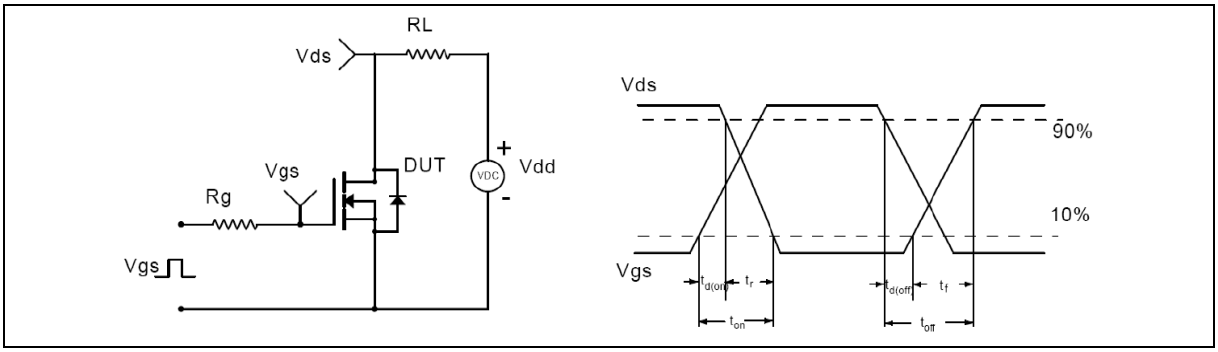


Figure 2. Switching time test circuit & waveforms

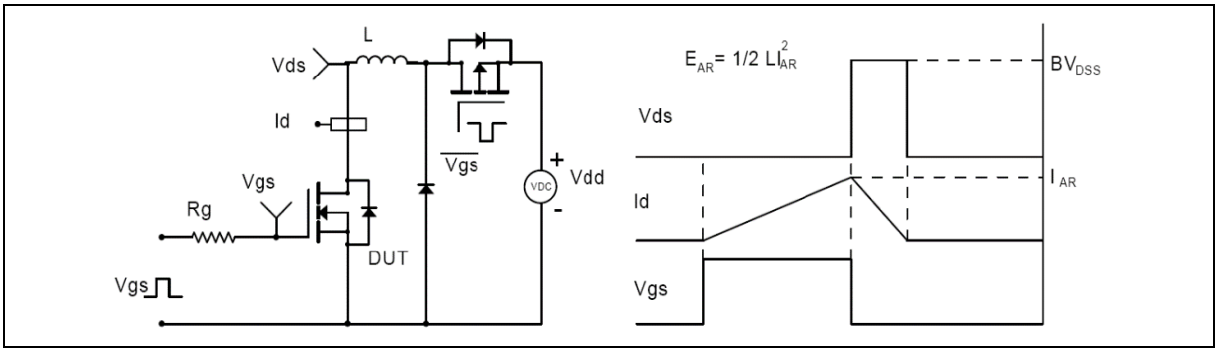


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

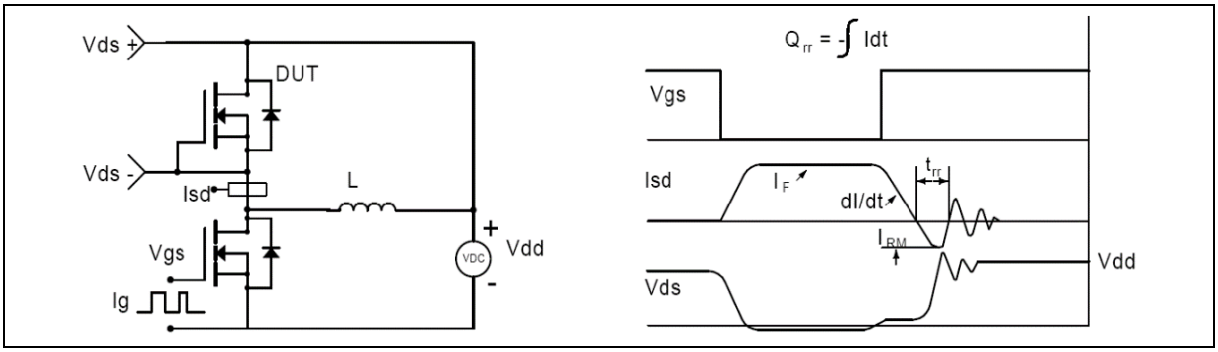
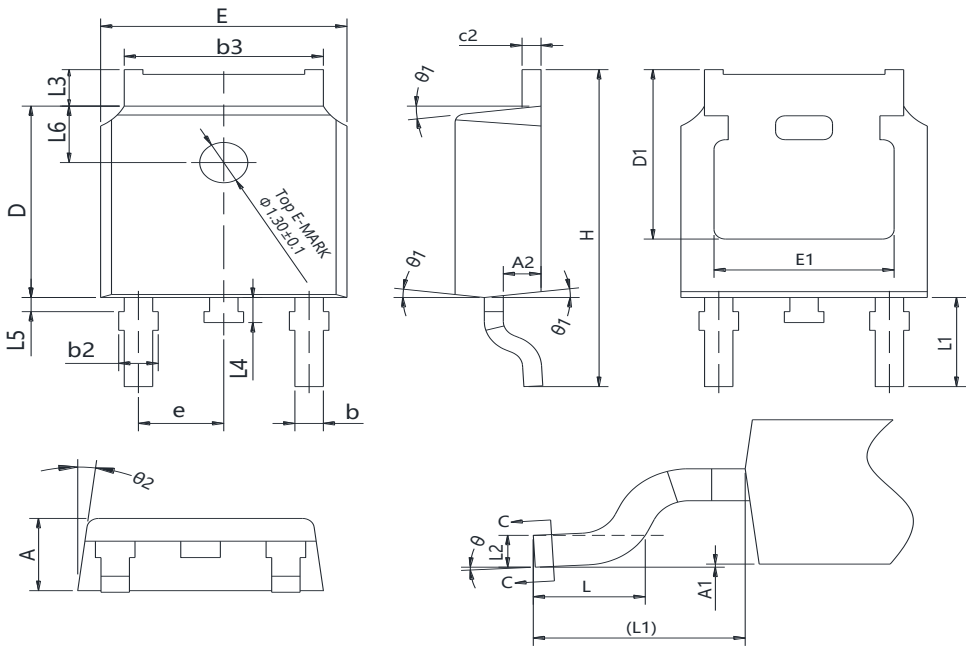


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b1	0.71	0.76	0.81
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.508BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80REF		
θ	0°	-	8°
θ1	5°	7°	9°
θ2	5°	7°	9°

Version 1: TO252-J package outline dimension

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Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-J	2500	2	5000	5	25000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG80R380DSF	TO252	yes	yes	yes

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